

# OBSERVER BASED POLE PLACEMENT AND LINEAR QUADRATIC OPTIMIZATION FOR DC-DC CONVERTERS

R.SHENBAGALAKSHMI<sup>1</sup> T.SREE RENGARAJA<sup>2</sup>

Mookambigai College of Engineering<sup>1</sup>, Pudukkottai, Anna University of Technology<sup>2</sup>,  
Tiruchirapalli

[lakshmi\\_amrith@yahoo.com](mailto:lakshmi_amrith@yahoo.com)<sup>1</sup>, [renga\\_raja@rediffmail.com](mailto:renga_raja@rediffmail.com)<sup>2</sup>

**Abstract:** A thorough and effective analysis of the DC-DC converters is carried out in order to achieve the system stability and to improve the dynamic performance. A small signal modeling based on state space averaging technique for DC-DC converters is carried out. A state feedback gain matrix (control law) and a full order state observer (which estimates all the state variables) are designed by pole placement technique in order to achieve the stability of a completely controllable system. Using Separation Principle, state feedback matrix and the full order state observer are combined together to provide a dynamic compensation for the dc-dc converters. The state feedback matrix is optimized using Linear Quadratic Optimal Regulator method based on Riccati matrix which further improves the system performance. The analysis and operation are investigated and verified through simulation. When compared with the conventional PID controller and other existing methods the output responses obtained by these methods are very much improved with zero output voltage ripples, zero peak overshoot, much lesser settling time in the range of ms and with higher overall efficiency (>90%).

**Key words:** DC-DC Converters, full order state observer, Pole Placement, state feedback gain matrix, Riccati matrix

## 1. Introduction

DC-DC converters are extensively used in distributed power supply system, which in turn are widely used in space stations, ships, aircrafts and telecommunication systems. They are also used extensively in Uninterruptible power supply, power factor improvement, harmonic elimination, fuel cells applications and in photovoltaic arrays.

An Observer Controller (which estimates the unmeasurable variables) with state feedback matrix (control law) has been designed for Buck and Boost converters using Pole Placement technique and Separation Principle. Observer is designed based on Kalman like filter which considerably decreases the noise sensibility of the inductor current. In other words it can be seen as a sensor less current mode controller [2].

The Buck and the Boost converters are modeled using state space averaging technique in which the design is based on the performance indices in time domain and hence the converter specifications are met. The unique feature of this method is that the design can be carried out for a class of inputs such as impulse, step or sinusoidal

function in which the initial conditions are also incorporated.

The Separation Principle makes the design procedure much simpler in which the state feedback gain matrix is designed by pole placement and then the full order state observer by the same technique, finally which can be combined together to provide a better dynamic compensation for both the buck and boost converters. The main advantage of this Principle is that the design of control law and the observer can be carried out independently and when both are used together the roots remain unchanged. Linear Quadratic Optimal Regulator method is used for output tracking which minimizes the performance indices. The control law is optimized by computing the Riccati matrix. The simulation is done using MATLAB/Simulink. The subsequent sections are subdivided as stated below.

The section 2 gives the Block diagram, section 3 gives the modeling and design details, section 4 gives the simulation results and discusses the results. The conclusion and the references are discussed in the sections 5 and 7 respectively.

## 2. Block Diagram of the Observer Controller

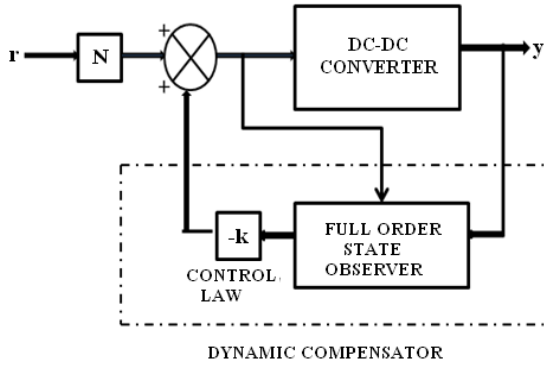


Fig. 1. Block diagram of the DC-DC Converter with Observer Controller

The Block diagram of the DC-DC converter with Observer Controller is shown in fig.1. The dynamic compensator has been designed for both the Buck and Boost converters using pole placement technique and Separation principle. The pole placement technique allows carrying out the design and analysis in time domain and hence the converter specifications such as settling time and maximum peak overshoot are achieved for step load response. A state feedback gain matrix  $[k_1 \ k_2]$  is designed based on the assumption that all the state variables are available for feedback and a full order state Observer is designed which are used to estimate all the state variables required to complete the design. Finally, a dynamic compensator is developed for the improvement of the dynamic performance and to achieve the stability of the systems under consideration.

## 3. Modeling and Design

### A. State Space Analysis for DC-DC Converters:

The state space analysis is carried out for both the buck and boost converters and is explained as follows:

For continuous time systems, the state equation and the output equation take the following form,

$$\left. \begin{aligned} \dot{x}(t) &= Ax(t) + Bu(t) \\ y(t) &= Cx(t) + Du(t) \end{aligned} \right\} \quad (1)$$

where  $x(t)$  is the state vector,  $A$  and  $C$  are the state coefficient matrices,  $u(t)$  is the source vector,  $B$  and  $D$

are the source coefficient matrices respectively for the continuous time systems.

The state models for Buck and Boost converters are presented now.

**Buck Converter:** The Buck converter requires only one switch and it is the simplest one, highly efficient more than 90% and it is shown in fig.2. The output voltage of the Buck converter is always less than the input voltage.

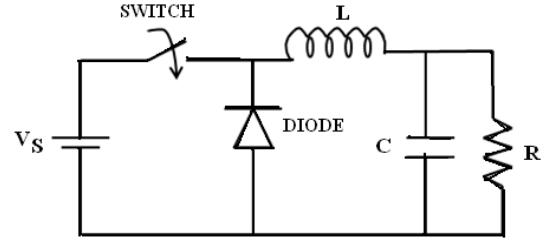


Fig. 2. Schematic diagram of Buck converter

The average output voltage of the Buck converter is given by,

$$V_o = V_s d \quad (2)$$

Where  $d$  is the duty cycle ratio and  $V_s$  is the input voltage. The typical state model of this converter with continuous time signal is described as follows,

$$\left. \begin{aligned} \begin{bmatrix} \dot{x}_1(t) \\ \dot{x}_2(t) \end{bmatrix} &= \begin{bmatrix} 0 & -1 \\ \frac{1}{L} & \frac{-1}{RC} \end{bmatrix} \begin{bmatrix} x_1(t) \\ x_2(t) \end{bmatrix} + \begin{bmatrix} \frac{d}{L} \\ 0 \end{bmatrix} V_s(t) \\ y(t) &= [0 \quad 1] \begin{bmatrix} x_1(t) \\ x_2(t) \end{bmatrix} \end{aligned} \right\} \quad (3)$$

where  $x_1$  and  $x_2$  are the average inductor current and average output capacitor voltage respectively.

**Boost Converter:** The Boost converter has a simple structure, continuous input current, step-up conversion ratio and it also has a clamped voltage stress and more over clamped switch voltage stress to the output voltage. It is shown in fig.3.

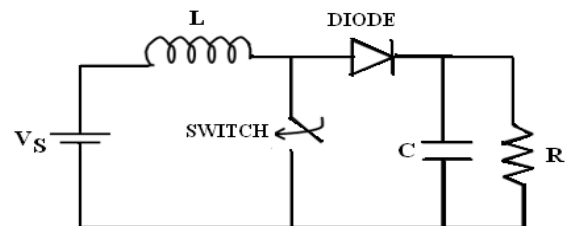


Fig. 3. Schematic diagram of Boost converter

The average output voltage of this converter is given by,

$$V_o = \frac{V_s}{1-d} \quad (4)$$

The state model of the Boost converter for continuous time signal is as follows:

$$\begin{cases} \begin{bmatrix} \dot{x}_1(t) \\ \dot{x}_2(t) \end{bmatrix} = \begin{bmatrix} 0 & \frac{d-1}{L} \\ \frac{1-d}{L} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} x_1(t) \\ x_2(t) \end{bmatrix} + \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} V_s(t) \\ y(t) = [0 \quad 1] \begin{bmatrix} x_1(t) \\ x_2(t) \end{bmatrix} \end{cases} \quad (5)$$

Table1  
Design Parameters of Buck and Boost Converters

Sl.No.	Parameter	Value	
		Buck Converter	Boost Converter
1	Magnetizing Inductance (L)	22.5mH	72 $\mu$ H
2	Output filter Capacitor (C)	0.28 $\mu$ F	217 $\mu$ F
3	Input Voltage (V <sub>s</sub> )	48V	24V
4	Output Power (P <sub>O</sub> )	24W	100W
5	Switching frequency(f <sub>s</sub> )	40kHz	20kHz
6	Load Resistance(R)	10 $\Omega$	23 $\Omega$

## B. Robust design of control law using Pole Placement Technique

The main objective is to choose the state feedback gain matrix based on control law given by  $u = -kx(t)$  for the systems under consideration defined by their respective state equations. The desired steady state value of the controlled variable  $y$  is a constant reference input  $r$ , which is taken as a unit step input. The design is carried out using the values shown in table 1. The root loci of both buck and boost converter topologies under consideration are drawn from which the desired closed loop poles are chosen for the design of the state feedback gain matrices of the converters. The necessary condition for the arbitrary pole placement is that the system should be completely controllable.

It is desired to place the closed loop poles arbitrarily in the  $s$ -plane for both the converters in

such a way that  $y(t)$  tracks any of the references  $r(t)$  which is considered as a step function in this case.

For the state equations (3) and (5) under consideration, if we assume that all the state variables are accurately measured at all times, a linear control law is possible to be implemented which is defined as  $u = -kx(t)$ . With this state feedback control law, the state equations of the systems under consideration take the form as follows:

$$\dot{x}(t) = (A - Bk)x(t) \quad (6)$$

If the systems under consideration are completely state controllable, then all the Eigen values of  $(A - Bk)$  is placed in the left half of  $s$ -plane and the closed loop system thus considered is asymptotically stable. The sufficient condition for the pole placement can be obtained, when the systems under consideration defined by the equations (3) and (5) for continuous time systems are transformed into controllable canonical forms. The transformation matrix  $T$  is given by the following equation,

$$T = MW \quad (7)$$

where  $M$  is the controllability matrix given by,

$$M = [B : AB : \dots : A^{n-1}B] \quad (8)$$

and

$$W = \begin{bmatrix} a_{n-1} & a_{n-2} & \dots & 1 \\ a_{n-2} & a_{n-3} & \dots & 0 \\ \vdots & \vdots & \ddots & \vdots \end{bmatrix} \quad (9)$$

Now the new state vector takes the form as,

$$x = T\tilde{x} \quad (10)$$

If the system is completely state controllable then the inverse of the matrix  $T$  exists and the system dynamic equation can be modified as follows:

$$\dot{\tilde{x}} = T^{-1}AT\tilde{x} + T^{-1}Bu \quad (11)$$

Now the systems under consideration are of second order and if the desired Eigen values in general are assumed as  $\mu_1, \mu_2, \mu_3, \dots, \mu_n$ , then the desired characteristic equation for finding the state feedback matrix are discussed now.

For continuous time systems, the desired characteristic equation is defined as,

$$s^n + \alpha_1 s^{n-1} + \dots + \alpha_{n-1} s + \alpha_n = 0 \quad (12)$$

Let,

$$kT = [\delta_n \quad \delta_{n-1} \quad \dots \quad \delta_1] \quad (13)$$

Now the system equation becomes,

$$\dot{\tilde{x}} = T^{-1}AT\tilde{x} - T^{-1}BkT\tilde{x} \quad (14)$$

Thus the characteristic equation is given by,

$$|sI - T^{-1}AT + T^{-1}BkT| = 0 \quad (15)$$

This equation takes the same form as that of the characteristic equation of the system, when  $u = -kx$  is used as a control signal. With further simplification in the controllable canonical form, the characteristic equation of the system attains the form as described as follows:

$$s^n + (a_1 + \delta_1)s^{n-1} + \dots + (a_{n-1} + \delta_{n-1})s + (a_n + \delta_n) = 0$$

(16)

By equating the like powers of  $s$  we can obtain the state feedback matrix which takes the following form:

$$k = [\delta_n \ \delta_{n-1} \ \dots \ \delta_1]T^{-1} \quad (17)$$

Thus all the Eigen values can be arbitrarily placed by choosing the matrix  $k$  using the above equation. The control scheme for the DC-DC converters is shown in fig.4.

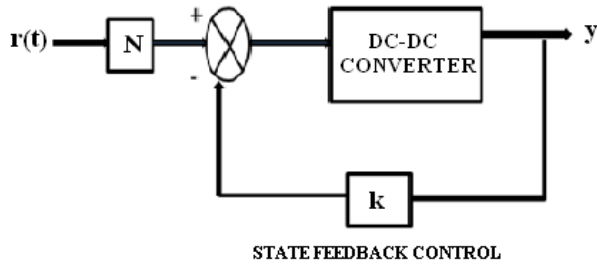


Fig. 4. Control scheme for continuous time system

Here  $N$  represents the scalar feed forward gain. It is evident from the fig.5 and fig.6 that the output  $y(t)$  for buck and boost converters track the unit step response respectively.

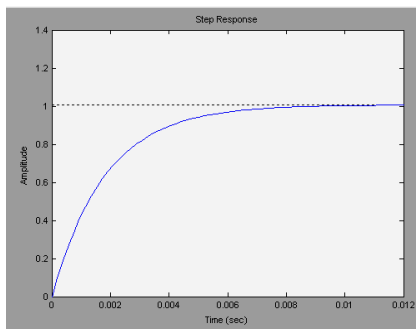


Fig. 5. Step response of Buck converter with state feedback matrix

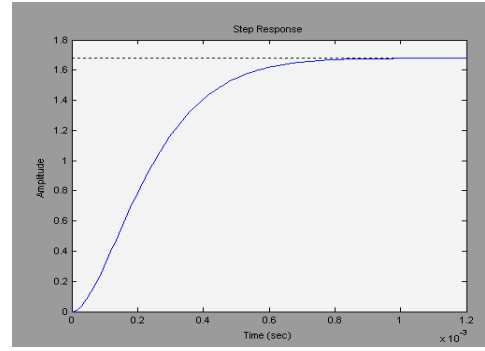


Fig. 6. Step response of Boost converter with state feedback matrix

### C.Design and Implementation of full-order Observer Controller

The exact dynamic model of the systems defined by the state equations can be designed using the observer controller in which it is designed using the same pole placement technique with the condition that the response of the observer should be faster than the response of the system, since the observer tends to act upon on the error of the system. The observability of the control systems under consideration should be checked which is one of the necessary conditions for the design of observer controller. By the thumb rule the desired observer location is made by having the following assumption:

Natural frequency of oscillation (observer controller)  $\approx 2$  to 5 times that of the Natural frequency of oscillation of the system.

Now the dynamic response of the system with full order state observer takes the following form,

$$\dot{\hat{x}}(t) = (A - Bk)\hat{x}(t) + Bk_1r \quad (18)$$

Where  $k_1$  is the element of the state feedback gain matrix and  $r$  is the step input.

The dynamics of the full order state observer for both the Buck and Boost converters are discussed below. The dynamic equation describing the state observer (Continuous time system) takes the following form,

$$\dot{\hat{x}}(t) = (A - k_e C)\hat{x} + Bu(t) + k_e y(t) \quad (19)$$

where  $k_e$  is the observer gain matrix. The required dynamic performance of the DC-DC converters are obtained by assuming the following desired characteristic equation,

$$\left. \begin{aligned} \text{Settling time} &\approx \frac{4}{\zeta \omega_n} < 1\text{ms} \\ \text{Maximum Peak Overshoot} &\leq 1\% \end{aligned} \right\} \quad (20)$$

The transfer function of the Observer Controller for the Buck and Boost converters thus designed are now discussed.

**Buck Converter:** The transfer function of the Observer Controller for this converter for continuous time system is obtained as follows,

$$\frac{U(s)}{-Y(s)} = \frac{-3.624 \times 10^8 s + 4.22 \times 10^{10}}{s^2 + 2.113 \times 10^5 s + 4.467 \times 10^{10}} \quad (21)$$

It is obvious from the fig.7 that the output thus obtained for this converter shows much lesser settling time, no overshoots or undershoots with zero steady state error.

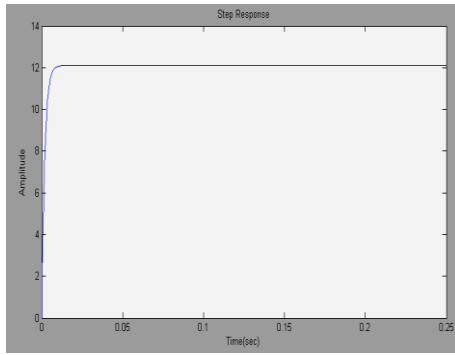


Fig. 7. Output of the Buck Converter with full order Observer Controller

**Boost Converter:** The transfer function of the Observer Controller for this converter for continuous time system is obtained as follows,

$$\frac{U(s)}{-Y(s)} = \frac{2.0937 \times 10^6 s + 1.449 \times 10^9}{s^2 + 15.45 \times 10^3 s + 1.4494 \times 10^{10}} \quad (22)$$

Similarly the output response of the Boost converter with full order state observer illustrated in fig.8 shows much improved converter specifications in which settling time is in the range of ms and more over no overshoots or undershoots are evident.

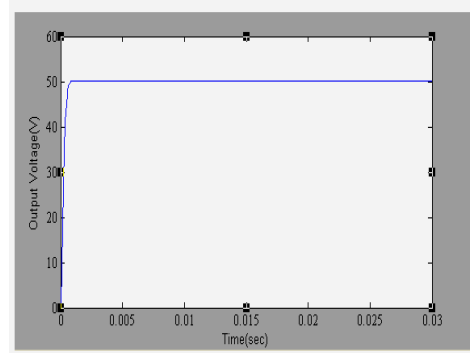


Fig. 8. Output of the Boost Converter with full order Observer Controller

#### D. Output tracking by Linear Quadratic Optimal Regulator (LQR)

The optimal regulator problems determine the state feedback matrix 'k' for obtaining the optimal control law given by  $u(t) = -kx(t)$ . The main objective is to minimize the performance index which is defined as follows,

$$J = \frac{1}{2} \int_0^{\infty} (x^T * Qx + u^T * Ru) dt \quad (23)$$

Here Q and R are the positive definite Hermitian symmetric matrix. The design of the regulator is carried out in following two steps:

(i) The positive definite Riccati matrix,  $P$  is determined which should satisfy the following reduced Riccati matrix equation given by,

$$A^T * P + PA - PBR^{-1}B^T * P + Q = 0 \quad (24)$$

For the appropriate  $P$  value  $(A-Bk)$  should be asymptotically stable.

(ii) Substitution of Riccati matrix in the equation described below results in the optimal  $k$  value.

$$k = -R^{-1}B^T * P \quad (25)$$

The response for the DC-DC converters with optimal state feedback gain matrix is shown compared with the Observer controller obtained by pole placement method and PID Controller in fig. (9) and fig. (10) respectively. It is evident that the LQR method shows better response with settling time in the range of ms, no peak overshoots or undershoots.

Table 2

Comparison of the Performance Parameters of DC-DC converters

Controller	Settling Time(s)		Peak Overshoot (%)		Steady State Error(V)		Rise Time(s)		Output Ripple Voltage(V)	
	Buck Converter	Boost Converter	Buck Converter	Boost Converter	Buck Converter	Boost Converter	Buck Converter	Boost Converter	Buck Converter	Boost Converter
<b>PID Controller</b>	10	10	2	2	0	0	3	3	0.01	0.01
<b>Observer Controller</b>	0.3	0.4	0	0	0	0	0.1	0.1	0	0
<b>Linear Quadratic Optimal Regulator</b>	0.01	0.01	0	0	0	0	0.005	0.005	0	0

Table 3

Output Response for Load Variations

Buck Converter					Boost Converter				
R ( $\Omega$ )	L ( $\mu$ H)	E (V)	Reference Voltage (V)	Output Voltage (V)	R ( $\Omega$ )	L ( $\mu$ H)	E (V)	Reference Voltage (V)	Output Voltage (V)
3.6	-	-	12	12.95	25	-	-	48	48
15	-	-	12	13	30	-	-	48	49
9	10	-	12	12.9	25	1	-	48	48
9	35	-	12	12.75	25	100	-	48	48.17
20	10	10	12	12.56	20	1	5	48	48.42
9	20	6	12	12.9	20	50	5	48	48.44



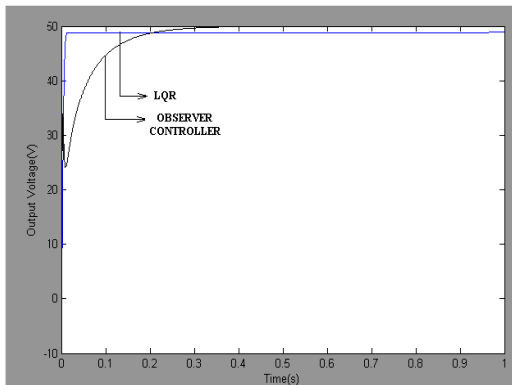


Fig. 9. Comparison of Observer controller with LQR

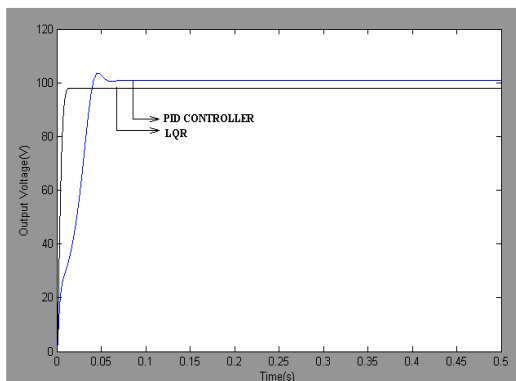


Fig. 10. Comparison of PID Controller with LQR

#### 4. Discussion

The DC-DC converters with Observer based controller and Linear Quadratic Optimal regulator are designed and simulated. The complete regulator along with the control law and the full order observer controller is simulated using both the state model and the transfer function model for voltage mode controlled Buck and current mode controlled Boost converter topologies and the outputs are shown. The simulated models show better results than the existing systems. The performance parameters have improved and it is tabulated in Table 2. The response of the system is much faster and it works well for all the possible values of the duty cycle and the change in the load as illustrated in Table 3. The simulations very well fit with the mathematical calculations and produce the desired DC-DC converter specifications.

#### 5. Conclusion

An observer based control approach for DC-DC converters using pole placement technique and Linear quadratic optimization has been

presented. The analysis and design are carried out based on the time domain and hence the controller is designed for the required specifications of both the converters. The designed values track the step references and the output voltage is completely regulated. The compensator thus designed very much looks like a classical one since the design is based on the simple root locus method. The Linear Quadratic Optimal Regulator is designed for the optimization of the  $k$  value and the results are illustrated. It is very well understood that the LQR method seems to be superior when compared with the other methods. This method can be extended for any of the applications such as power factor preregulation, photovoltaic cell and speed control applications and can also be applied to other converter topologies such as Buck-Boost, Cuk, and SEPIC.

#### 6. Nomenclature

$x(t)$	State vector
$A, C$	State Coefficient matrices
$B, D$	Source Coefficient matrices
$V_s$	Input Voltage
$V_o$	Output Voltage
$d$	duty cycle ratio
$x_1$	average inductor current
$x_2$	average output capacitor voltage
$R$	Load Resistance
$C$	Capacitance
$L$	Inductance
$y(t)$	Controlled Variable
$u$	control law
$k$	state feedback gain matrix
$r$	reference input
$T$	transformation matrix
$M$	controllability matrix
$\mu$	desired eigen value
$N$	scalar feed forward gain
$k_1$	element of the state feedback gain matrix
$k_e$	observer gain matrix
$\zeta$	damping ratio
$\omega_n$	natural frequency of oscillation
$J$	performance index
$Q, P$	Positive definite Hermitian Symmetric matrix
$R$	Riccati matrix

#### 7. References

- [1]Guang Feng et al.: *A digital Control Algorithm for DC-DC converters Under Input Voltage Change*. In: Proceedings of the 21<sup>st</sup> Annual IEEE Applied Power Electronics Conference and Exposition APEC'06, March 19-23, 2006, p.1430 –1436.
- [2] N.Gonzalez – Fonseca, J.de Leon – Morales and J.Leyva- Ramos: *Observer-Based Controller for Switch Mode DC-DC Converter*. In: Proceedings of the 44<sup>th</sup> IEEE Conference on Decision and control and

European Control Conference CDC-ECC '05, Dec.12-15, 2005, p. 4773 – 4778.

[3] H.S. Bae, J.H. Yang, J.H.Lee and Bo H.Cho: *Digital State Feedback Current Control using Pole Placement Technique for the 42V/14V Bi-Directional DC-DC Converter Application*. In: Proceedings of 22<sup>nd</sup> Annual IEEE Applied Power Electronics Conference APEC 2007, Feb. 25 – March 01, 2007, p.3-7.

[4] M.Gopal.: *Digital Control and State Variables Methods Conventional and Intelligent Control Systems*, Tata McGraw Hill Publishing Company Limited, 2009

[5] Katsuhiko Ogata.: *Discrete-Time control Systems*, PHI Learning Private Limited, 2009

[6] Wangfeng zhang et al.: *A direct duty cycle calculation algorithm for digital power factor correction (PFC) implementation*. In: Proceedings of 35<sup>th</sup> Annual Power Electronics Specialist Conference PESC'04, June 20-25, 2004, p.2326-2332.

[7] Wangfeng zhang et al.: *A digital Power Factor Correction Control Strategy Optimized for DSP*. In: IEEE Transactions on Power Electronics, IXX (2004), No.6, Nov. 2004, and p. 1474-1485.

[8] Jeffrey Morroni et al. : *Design and Implementation of an Adaptive Tuning System Based on Desired Phase margin for Digitally Controlled DC – DC Converters*. In: IEEE Transactions on Power Electronics, XXIV (2009), No.2, Feb. 2009, p.559-564

[9] Jong-Won Shin et al.: *Digital Average Current Mode Control of Boost Converter Using Diode Current Sensing Technique*. In: Proceedings of Power Electronics Annual Conference KIPE '09, July 2009, p.305-307.

[10] Mohd. Sai Fuzam Jamri an Tan Chee Wei.: *Modeling and control of a photovoltaic Energy System using State-Space Averaging Technique*. In: American Journal of Applied Sciences 7(5), 2010, p.682-691.



**Dr.Sree Renga Raja** is working as an Assistant Professor in the Department of Electrical and Electronics Engineering, Anna University of Technology, Tiruchirapalli, Tamil Nadu, India. He obtained B.E (Electrical and Electronics Engineering) degree from Manonmaniam Sundaranar University, Tirunelveli in 1998, M.E (Power Systems) from Annamalai University, Chidambaram in 1999 and Ph.D from Anna University, Chennai in 2007. He has published many papers in the field of Power System Engineering. His area of interest includes Power system optimization, Renewable Energy Applications, Energy Conservation Management and Insulation Engineering.

#### About the Authors



**R.Shenbagalakshmi** is pursuing research from Anna University of Technology, Tiruchirapalli, Tamil Nadu, India. She obtained B.E (Electrical & Electronics Engineering) degree from Bharathidasan University, Tiruchirapalli in 2001 and Master degree from Vinayaga Missions University, Salem in 2007. Her area of research is DC-DC converters.